

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A radio base station comprising:
a monitor,
memory and
one or more resources, said memory being connected to the monitor and arranged for storing tasks and data, each of said resources being connected to the monitor and arranged for at least one of performing an arithmetic function and executing a program,
at least one analog signal manifold comprising
input lines, output lines, and nodes for making connections between input and output lines, said input lines and output lines being connectable to predetermined resources and said nodes being arranged to perform a mathematical operation on an incoming signal on the input lines.
2. (Previously Presented) The radio base station according to claim 1, wherein said resources that are arranged to execute a program are also arranged to generate trigger signals and send them to the monitor, said monitor being arranged to receive said trigger signals, to read one or more tasks related to said trigger signals from said memory, to check whether resources required for performing said task are available and to send commands to selected resources specifying the task to be performed.
3. (Previously Presented) The radio base station according to claim 1, wherein connections between said memory and said monitor, and connections between said resources and said monitor are implemented by means of a bus.

4. (Previously Presented) The radio base station according to claim 3, wherein said resources are arranged for mutual communication via said bus.

5. (Previously Presented) The radio base station according to claim 3, wherein using the bus is based on a datagram principle.

6. (Previously Presented) The radio base station according to claim 1, wherein said memory comprises a task memory and a data memory.

7. (Previously Presented) The radio base station according to claim 1, wherein said monitor comprises a state machine sequencer for handling several state machines in parallel.

8. (Previously Presented) The radio base station according to claim 7, wherein said memory comprises a ROM portion and a RAM portion, said ROM portion storing state machine definitions for said state machine sequencer, task definitions and default structures, said RAM portion storing dynamic data.

9. (Previously Presented) The radio base station according to claim 8, wherein said RAM portion stores a resource allocation table, a data block list, and data blocks.

10. (Previously Presented) The radio base station according to claim 1, wherein said monitor comprises an executor arranged for:

- sending commands to resources;
- sending task block requests to memory;
- receiving status information from resources; and
- receiving task blocks from memory.

11. (Previously Presented) The radio base station according to claim 9, wherein said monitor comprises an executor arranged for:

- sending commands to resources;
- sending task block requests to memory;
- receiving status information from resources;
- receiving task blocks from memory; and
- maintaining said resource allocation table.

12. (Previously Presented) The radio base station according to claim 1, wherein said one or more resources comprises at least one of:

- a transmitter,
- a receiver,
- a digital analog converter,
- an analog digital converter,
- a control unit, and
- a digital signal processor.

13. (Previously Presented) The radio base station according to claim 12, wherein said one or more resources comprise at least one digital signal processor storing an executable image for performing a program.

14. (Previously Presented) The radio base station according to claim 12, wherein said one or more resources comprise

- a plurality of transmitters,
- a plurality of receivers,
- a plurality of digital analog converters, and
- a plurality of analog digital converters, said at least one analog signal manifold being arranged for making connections between said plurality of transmitters and said plurality of digital analog converters, and for making connections between said plurality of receivers and said plurality of analog digital converters.

15. (Previously Presented) The radio base station according to claim 1, wherein said mathematic operations comprise at least one of multiplying, adding, subtracting, and one-to-one connecting.

16. (Previously Presented) A method of operating a radio base station having a monitor, memory, one or more resources and at least one analog signal manifold, said memory being connected to the monitor and storing tasks and data, each of said one or more resources being connected to the monitor, said at least one analog signal manifold comprising input lines, output lines, and nodes for making connections between input and output lines, said input lines and output lines being connected to predetermined resources, said method comprising:

performing one of an arithmetic function and executing a program by said one or more resources,

reading one or more tasks from said memory,

checking whether resources required for performing said one or more tasks are available and sending commands to selected resources specifying the task to be performed;

connecting one or more input lines with one or more output lines of the analog signal manifold by means of said nodes; and

performing at least a mathematical operation on an incoming signal on the input lines in said nodes.

17. – 18. (Canceled)

19. (Previously Presented) The method of claim 16, wherein connections between said memory and said monitor and connections between said resources and said monitor are implemented by means of a bus and said resources are arranged for mutual communication via said bus.

20. (Previously Presented) The method of claim 16, wherein said monitor comprises a state machine sequencer for handling several state machines in parallel.

21. (Previously Presented) The radio the method of claim 16, further comprising
storing a resource allocation table, a data block list and data blocks in a RAM portion of said memory, which comprises a ROM portion and the RAM portion, said ROM portion storing state machine definitions for said state machine sequencer, task definitions and default structures, said RAM portion storing dynamic data.

22. (Previously Presented) The method of claim 16, further comprising:
sending commands to resources;
sending task block requests to memory;
receiving status information from resources; and
receiving task blocks from memory.

23. (Previously Presented) The method of claim 16, the analog signal manifold being arranged for
making connections between said plurality of transmitters and said plurality of digital analog converters, and
making connections between said plurality of receivers and said plurality of analog digital converters.

24. (Previously Presented) The method of claim 16, wherein the step of performing a mathematical operation includes at least one of multiplying, adding, subtracting, and one-to-one connecting.